

1 **ABSTRACT OF DISCLOSURE**

2 A host system for generating a software built-in self-test engine (SBE) is provided for
3 enabling on-chip generation and application of a re-generative functional test on a complex
4 device such as a microprocessor under test. The host system comprises user directives provided
5 to indicate user desired actions; instruction information provided to define a suite of instructions;
6 and a SBE generation tool arranged to generate a software built-in self-test engine (SBE) based
7 on the user directives, the instruction information and device constraints, for subsequent storage
8 on-board of a complex device such as a microprocessor under test and activation of a re-
9 generative functional test on the complex device under test (DUT).

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